REMARKS

By the present Amendment, claim 1 has been cancelled. Claims 2-4 are newly presented for consideration. Accordingly, claims 2-4 are now pending in the application.

In the Office Action of January 11, 2006, claim 1 was rejected under the judicially created ground of obviousness type double patenting as being unpatentable over U.S. Patent No. 6,661,397. Claim 1 was also rejected under 35 USC §102(e) as being anticipated by U.S. Patent No. 5,798,746 issued to Koyama.

The cancellation of claim 1 renders these particular grounds of rejection moot.

Claims 2-4 are newly presented and recite features that are not taught or suggested by the art of record. For example, independent claim 2 defines an emissive display having pixels enclosed by a plurality of scan lines and a plurality of signal lines that intersect with each other. The emissive display is configured such that:

wherein each pixel includes a memory circuit and an inverter circuit, said inverter circuit including an electroluminescent device formed by an organic multilayers driven by a current as a load device, and

said memory circuit stores display information of said pixel according to a conduction state or a non-conduction state of a main circuit of the inverter, and controls an on state and an off state of said electroluminescent device on a binary basis.

As set forth in independent claim 2, each pixel includes a memory circuit and an inverter circuit. The inverter circuit includes an electroluminescent device formed by inorganic multilayers driven by a current as a load device. Additionally, the memory circuit stores display information of the pixel according to a conduction state

or a nonconduction state of a main circuit of the inverter. Further, the memory circuit controls an "on" state and an "off" state of the electroluminescent device on a binary basis. According to the invention defined by independent claim 2, each pixel includes a single inverter composed of TFT and EL elements. See Fig. 2.

The Office Action had previously indicated that Koyama discloses the features recited in independent claim 1. However, independent claim 2 recites features that are not shown or suggested by Koyama. Koyama discloses a liquid crystal display device wherein time gradation display is used. A digital gradation signal on a signal line is supplied to the digital memory circuit arranged in the vicinity of each pixel electrode and stored therein for a desired period of time. The liquid crystal display device of Koyama provides a static memory that comprises two CMOS inverters for each pixel. This allows an output of the inverter to drive the liquid crystal element. Koyama does not appear to disclose a display device wherein each pixel includes only one inverter circuit and wherein the inverter includes a TFT and an OLED.

It is therefore respectfully submitted that independent claim 2 is allowable over the art of record.

Claims 3 and 4 depend from independent claim 2, and are therefore believed allowable for at least the reasons set forth above with respect to independent claim 2. In addition, these claims each introduce novel elements that independently render them patentable over the art of record.

For the reasons stated above, it is respectfully submitted that all of the pending claims are now in condition for allowance. Therefore, the issuance of a Notice of Allowance is believed in order, and courteously solicited.

If the Examiner believes that there are any matters which can be resolved by way of either a personal or telephone interview, the Examiner is invited to contact Applicants' undersigned attorney at the number indicated below.

AUTHORIZATION

Applicants request any shortage or excess in fees in connection with the filing of this paper, including extension of time fees, and for which no other form of payment is offered, be charged or credited to Deposit Account No. 01-2135 (Case: 500.40567CX1).

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP.

Leonid D. Thenor
Registration No. 39,397

LDT/vvr
1300 N. Seventeenth Street
Suite 1800
Arlington, Virginia 22209
Tel: 703-312-6600
Fax: 703-312-6666

Dated: April 11, 2006